

3-TERMINAL POSITIVE REGULATOR

1.Description

The FLD7815D is three-terminal positive regulator. One of these regulators can deliver up to 1A of output current. When used as a replacement for a Zener diode-resistor combination, an effective improvement in output impedance can be obtained, together with lower quiescent current.

2.Features

- Output Current of 1A
- Output transistor safe area protection
- No external components
- Package: TO-252

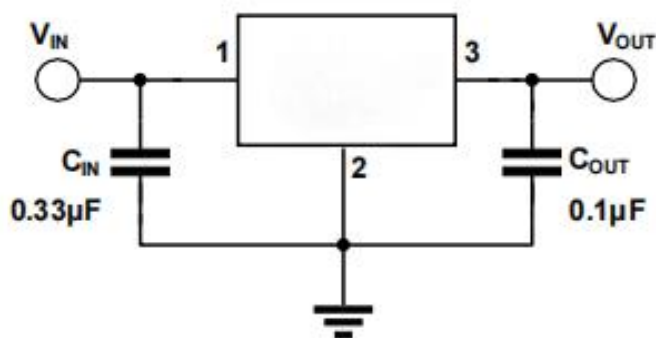
3.Application

- DC Motor Drivers
- Household Electric Appliances
- HVAC Systems
- Industrial Power Supplies
- SMPS Post Regulation
- Solar Energy String Invertors
- Test and Measurement Equipment

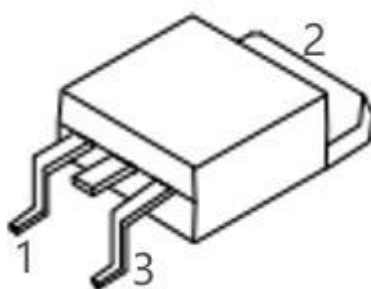
4.Order information

Mode	Package	Ordering Number	Packing Option
FLD7815D	TO-252	FLD7815DYTO252G/TR	Tape and Reel,2500

5.Typical Applications Circuit

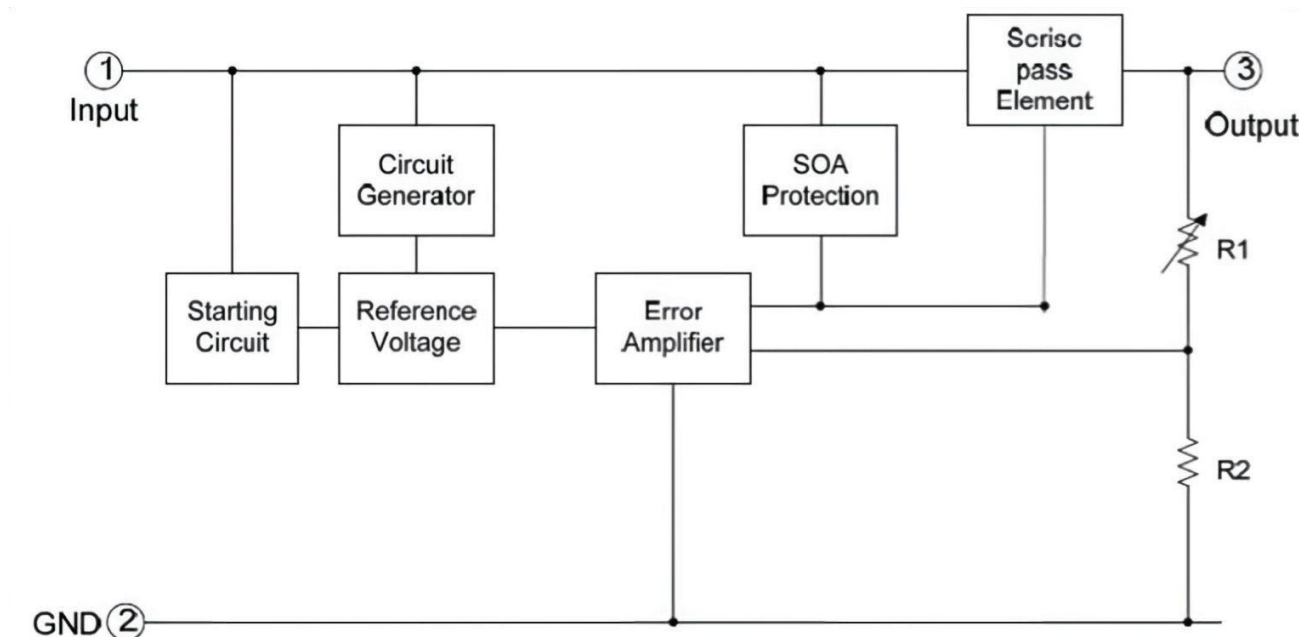


6.Pin Assignments



Pin No.	Pin Name	Description
1	VIN	Input voltage pin
2	GND	Ground pin
3	VOUT	Output voltage pin

7. Block Diagram



8. Absolute Maximum Ratings

(Ta=25℃)

Parameter	Rating	Unit
Input supply voltage: VIN	40	V
Max Output current: Iout	1	A
Max Power: Pmax	1	W
Max junction temperature: Tj	-25~125	℃
Storage temperature: Tstr	-55~125	℃
Soldering temperature and time	+260(Recommended 10S)	℃

Note: The absolute maximum ratings are rated values exceeding which the product could suffer physical damage.

These values must therefore not be exceeded under any conditions.

9. Electrical Characteristics

(C_{in}=0.33uF, C_o=0. 1uF, T_J = 25°C, unless otherwise specified)

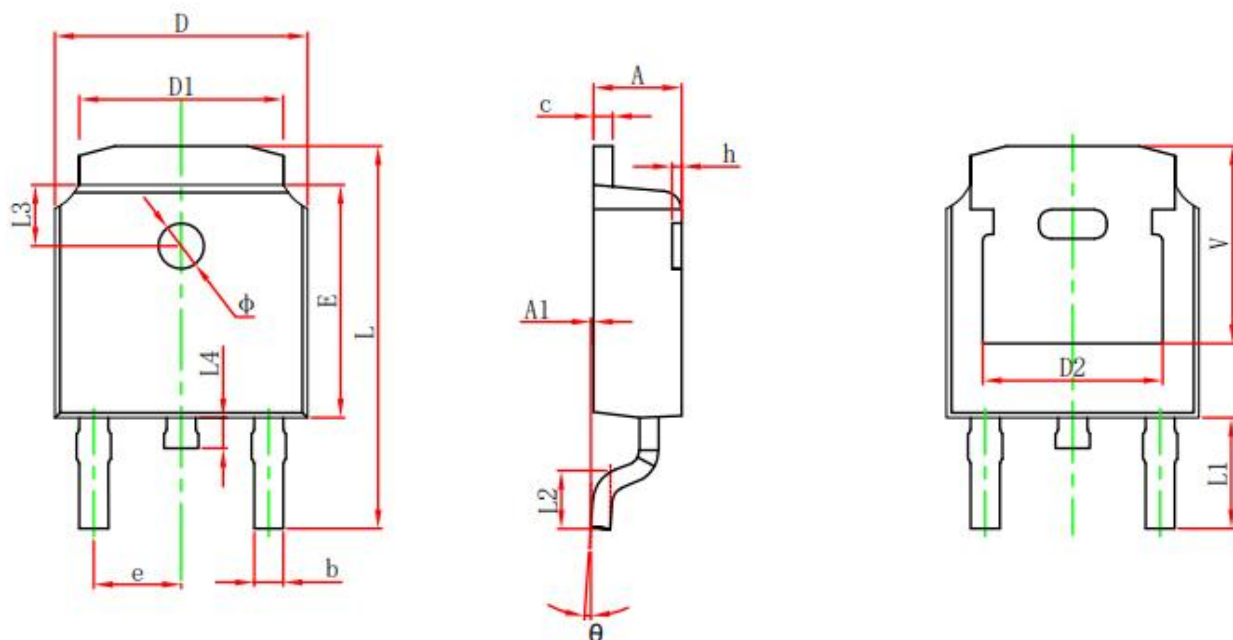
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Output Voltage	V _{OUT}	I _o =200mA, V _{in} = 20V	14.46	15.0	15.54	V
		I _o =100mA, V _{in} = 17 to 25V	14.4	15.0	15.6	
		I _o =1mA to 300mA, V _{in} = 18V	14.25	15.0	15.75	
Line regulation	LNR	I _o =20mA, V _{in} = 17 to 30V		60		mV
Load Regulation	LDR	I _o =1mA to 500mA, V _{in} = 20V		130		mV
		I _o =1mA to 40mA, V _{in} = 20V		40		
Drop Voltage	V _{DIF}	T _a =25°C, I _o =100mA		1.6		V
Ripple Rejection	PSRR	T _a =25°C, f=120Hz, I _o =100mA, V _{in} = 20V		55		dB
Quiescent Current	I _Q	I _o =40mA, V _{in} = 20V		2		mA
Quiescent Current Change	ΔI _Q	I _o =1mA, V _{in} = 17 to 30V		0.1		mA
		I _o =1mA to 100mA, V _{in} = 20V		0.1		mA

LNR: Line Regulation. The change in output voltage for a change in the input voltage. The measurement is made under conditions of low dissipation or by using pulse techniques such that the average chip temperature is not significantly affected.

LDR: Load Regulation. The change in output voltage for a change in load current at constant chip temperature.

9.Package Outline Dimensions

(1) Package Type: TO-252



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.635	0.770	0.025	0.030
c	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	4.830 REF.		0.190 REF.	
E	6.000	6.200	0.236	0.244
e	2.186	2.386	0.086	0.094
L	9.712	10.312	0.382	0.406
L1	2.900 REF.		0.114 REF.	
L2	1.400	1.700	0.055	0.067
L3	1.600 REF.		0.063 REF.	
L4	0.600	1.000	0.024	0.039
Φ	1.100	1.300	0.043	0.051
θ	0°	8°	0°	8°
h	0.000	0.300	0.000	0.012
V	5.250 REF.		0.207 REF.	

10.Important Notice And Disclaimer

- We reserves the right to change the instruction manual without prior notice.
- Any semiconductor product has a certain possibility of failure or malfunction under specific conditions. The buyer is responsible for complying with safety standards and taking safety measures when using our products for system design and overall manufacturing to avoid potential failure risks that may cause personal injury or property damage.
- The improvement of product quality is endless, our company will be dedicated to provide customers with better products.